



#### DMG6402LVT

April 2019

#### 30V N-CHANNEL ENHANCEMENT MODE MOSFET

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON) max</sub>	I <sub>D</sub> T <sub>A</sub> = +25°C
001/	$30m\Omega$ @ $V_{GS} = 10V$	6A
30V	42mΩ @ V <sub>GS</sub> = 4.5V	5A

### **Description**

This new generation MOSFET has been designed to minimize the onstate resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

#### **Applications**

- DC-DC Converters
- **Power Management Functions**
- Backlighting

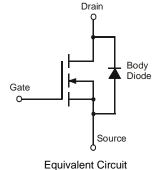
### **Features and Benefits**

- Low Input Capacitance
- Low On-Resistance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

#### **Mechanical Data**

- Case: TSOT26
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Tin Finish Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.013 grams (Approximate)

# TSOT26 Gate 4 I S Top View Top View Pin Configuration



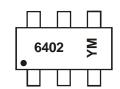
### **Ordering Information** (Note 4)

Part Number	Case	Packaging
DMG6402LVT-7	TSOT26	3,000/Tape & Reel
DMG6402LVT-13	TSOT26	10.000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**



6402 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: G = 2019)M = Month (ex: 9 = September)

Date Code Key

Year	201	1	~		2019	20	20	2021		2022		2023
Code	Υ		~		G		Η			J		K
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



### **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V <sub>DSS</sub>	30	V		
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
Steady $T_A = +25^{\circ}C$ State $T_A = +70^{\circ}C$		I <sub>D</sub>	6.0 4.8	А	
Continuous Drain Current (Note 5) V <sub>GS</sub> = 10V	t < 10s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I <sub>D</sub>	7.5 5.9	А
$\begin{array}{cccccccccccccccccccccccccccccccccccc$			I <sub>D</sub>	5.0 4.0	А
Continuous Drain Current (Note 5) V <sub>GS</sub> = 4.5V	t < 10s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I <sub>D</sub>	6 4.8	А
Maximum Body Diode Forward Current (Note 5)	Is	2	A		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	31	А		

### Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Total Power Dissipation (Note 5)	$T_A = +25$ °C	C-	1.75	W	
Total Fower Dissipation (Note 3)	$T_A = +70^{\circ}C$	P <sub>D</sub>	1.1	VV	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	0	72	°C/W	
Thermal Resistance, Junction to Ambient (Note 5)	t < 10s	$R_{\theta JA}$	50		
Thermal Resistance, Junction to Case (Note 5)	$R_{ heta JC}$	23			
Operating and Storage Temperature Range		$T_{J_i}T_{STG}$	-55 to +150	°C	

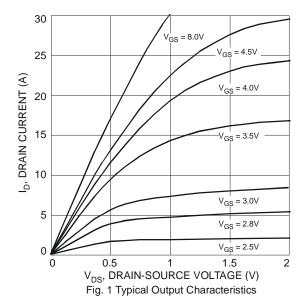
# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

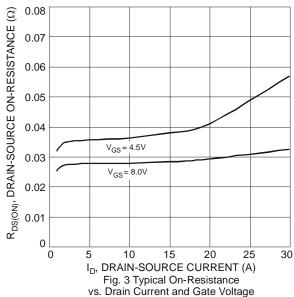
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 6)								
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1	μΑ	$V_{DS} = 30V, V_{GS} = 0V$		
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$		
ON CHARACTERISTICS (Note 6)								
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1	1.5	2	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$		
Static Drain-Source On-Resistance			22	30	mΩ	$V_{GS} = 10V, I_D = 7A$		
Static Diani-Source On-Nesistance	R <sub>DS(ON)</sub>		32	42	11122	$V_{GS} = 4.5V, I_D = 5.6A$		
Forward Transfer Admittance	Y <sub>fs</sub>		10	_	S	$V_{DS} = 5V, I_{D} = 7A$		
Diode Forward Voltage	$V_{SD}$	_	0.75	1.0	V	$V_{GS} = 0V$ , $I_S = 1A$		
DYNAMIC CHARACTERISTICS (Note 7)								
Input Capacitance	Ciss		498	_		15)/ )/ 0)/		
Output Capacitance	Coss	_	52	_	pF	$V_{DS} = 15V, V_{GS} = 0V$ f = 1.0MHz		
Reverse Transfer Capacitance	C <sub>rss</sub>	_	45	_		1 = 1.0IVII IZ		
Gate Resistance	Rg	_	2.4	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$		
Total Gate Charge	$Q_g$	_	11.4	_				
Gate-Source Charge	Qgs	_	1.4	_	nC	$V_{GS} = 10V, V_{DS} = 15V, I_D = 5.8A$		
Gate-Drain Charge	Q <sub>gd</sub>	_	2	_				
Turn-On Delay Time	t <sub>D(ON)</sub>	_	3.4	_				
Turn-On Rise Time	t <sub>R</sub>	_	6.2	_		$V_{DD} = 15V, V_{GS} = 10V,$		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	13.9		ns	$R_L = 2.6\Omega$ , $R_G = 3\Omega$		
Turn-Off Fall Time	t <sub>F</sub>	_	2.8	_				

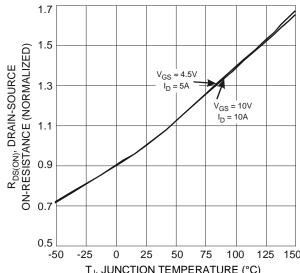
Notes:

- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1inch square copper plate.
- 6. Short duration pulse test used to minimize self-heating effect.
- 7. Guaranteed by design. Not subject to production testing.

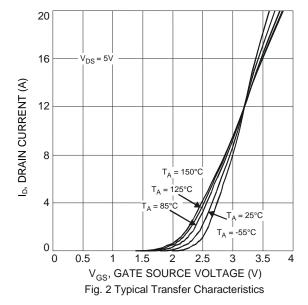








T<sub>J</sub>, JUNCTION TEMPERATURE (°C) Fig. 5 On-Resistance Variation with Temperature



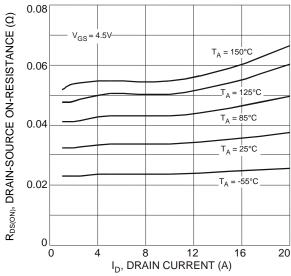
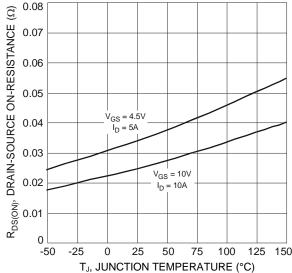


Fig. 4 Typical Drain-Source On-Resistance vs. Drain Current and Temperature



T<sub>J</sub>, JUNCTION TEMPERATURE (°C) Fig. 6 On-Resistance Variation with Temperature



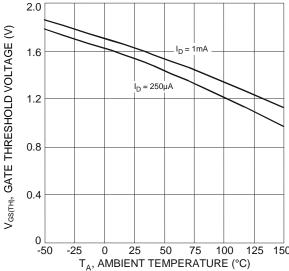
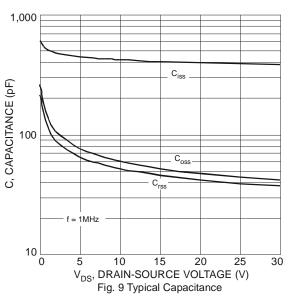
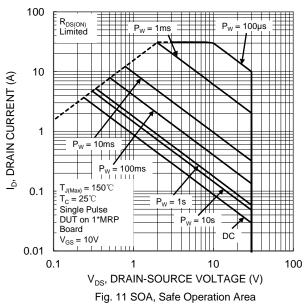
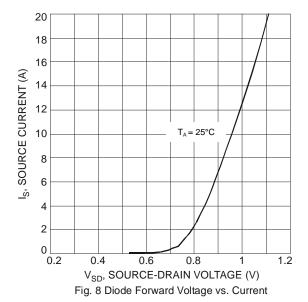
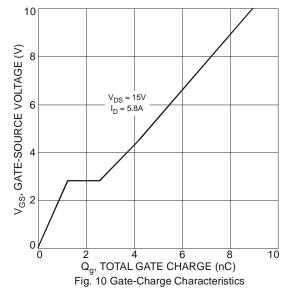


Fig. 7 Gate Threshold Variation vs. Ambient Temperature











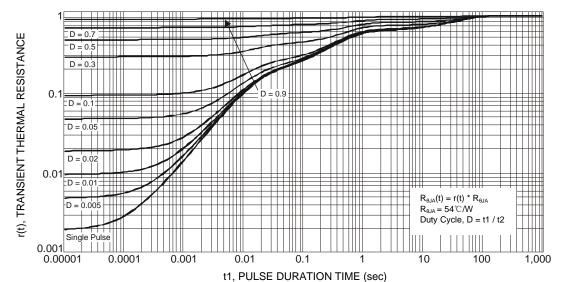


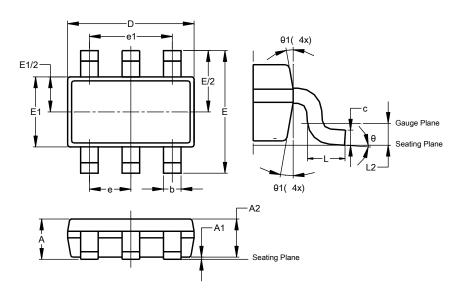
Fig. 12 Transient Thermal Resistance



### **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### TSOT26

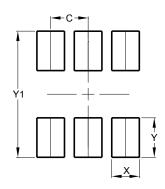


TSOT26							
Dim	Min Max Typ						
Α	-	1.00	-				
A1	0.010	0.100	-				
A2	0.840	0.900	_				
D	2.800	3.000	2.900				
Е	2	2.800 BS	C				
E1	1.500	1.700	1.600				
b	0.300	0.450	-				
С	0.120	0.200	-				
е	0.950 BSC						
e1	1	.900 BS	C				
L	0.30	0.50	-				
L2	0.250 BSC						
θ	0° 8° 4°						
θ1	4°	12°	-				
All Dimensions in mm							

### **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### TSOT26



Dimensions	Value (in mm)
С	0.950
Х	0.700
Y	1.000
Y1	3.199



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